

8 Block Diagram

30	VLCD5V
29	VLCD5V
28	VLCD5V
27	NC
26	NC
25	NC
24	GND
23	RXE3+
22	RXE3-
21	RXEC+
20	RXEC-
19	RXE2+
18	RXE2-
17	GND
16	RXE1+
15	RXE1-
14	GND
13	RXE0+
12	RXE0-
11	RXO3+
10	RXO3-
9	RXOC+
8	RXOC-
7	GND
6	RXO2+
5	RXO2-
4	RXO1+
3	RXO1-
2	RXO0+
1	RXO0-

CN104( Connected with LVDS Cable)

6	GND
5	GND
4	ON/OFF
3	Brightness
2	VCC5V
1	VCC5V

CN101( Connected with the Inverter Cable)

Main board

( Connected with function wire) CN106

1
2
3
4
5

CN103( Connected with the D-Sub Cable)

Data clock line (SC	15
VSYNC	14
H / H+V SYNC	13
Serial data (SDA)	12
NC	11
Cable Detect	10
NC	9
Blue GND	8
Green GND	7
Red GND	6
GND	5
NC	4
Blue video input	3
Green video input	2
Red video input	1

## Memo